

# **ELECTRONICS & COMPUTER ENGINEERING DEPARTMENT**

### (COURSE OUTLINE)

### ELEN 144 SEMICONDUCTOR DEVICES 1

This course is an introduction to semiconductor devices. It includes such topics ranging from semiconductor theory, diodes, transistors, and thyristors, to analogs IC's including op-amps and linear regulators. The focus of this course is in the application of these devices and troubleshooting circuits containing these devices.

OFFERED: CREDIT: IN-CLASS WORKLOAD: OUT-OF-CLASS WORKLOAD: PRE OR COREQUESITES:	First Semester 4 4 Lecture, 2 Lab for 6 ELEN 142	14 weeks
INSTRUCTOR INFORMATION:	John Yang, 250-370-4213,	Room: TEC 268 <u>yang@camosun.bc.ca</u>

#### **OBJECTIVE:**

Upon completion of this course, the student will have an understanding of commonly used analog electronic components and circuits.

#### **OUTLINE:**

- 1. Diodes
  - 1.1. The conductor and insulator
  - 1.2. Doping The semiconductor
  - 1.3. N and P type materials
  - 1.4. Biasing the PN Junction
  - 1.5. Diode characteristics
  - 1.6. Zener Diode characteristics
  - 1.7. Light-emitting diodes
  - 1.8. Photo diodes and laser diodes
  - 1.9. Schottky diodes
  - 1.10. Varactor, Tunnel and other miscellaneous diodes
  - 1.11. Varistor and other commonly used diodes
  - 1.12. Half and full wave rectifier circuits
  - 1.13. Diode Applications

(6 hrs)

2.	2.1.         BJT c           2.2.         Biasim           2.3.         BJT c           2.4.         Tempo	to Bipolar Transistors (BJTs) onstruction ag BJT's haracteristics erature effects on biasing voltages leshooting transistor bias circuits	(4 hrs)
3.	3.2.         Comm           3.3.         Comm           3.4.         Class           3.5.         Class           3.6.         Class	C Amplifiers non emitter amplifier non collector amplifier non base amplifier A amplifiers B amplifiers C amplifiers of distortion	(6 hrs)
4.	<ul> <li>4.1. JFET</li> <li>4.2. JFET</li> <li>4.3. JFET</li> <li>4.4. D type</li> <li>4.5. E type</li> </ul>	Fransistors (FETs) characteristics biasing amplifiers e MOSFETs e MOSFETs FET amplifiers	(6 hrs)
5.	<ul> <li>5.1. Low F</li> <li>5.2. High I</li> <li>5.3. Total</li> <li>5.4. Freque</li> </ul>	equency Response Frequency Amplifier Response Frequency Amplifier Response Amplifier Frequency Response ency Response of Multistage Amplifiers hing characteristics	(4 hrs)
6.	<ul> <li>6.1. Differ</li> <li>6.2. Ideal 0</li> <li>6.3. Block</li> <li>6.4. Op-an</li> </ul>	<ul> <li>to Operational Amplifiers (Op-Amps) ential amplifier (with a pair of transistors) Op-amps versus practical Op-amps diagram versus integrated circuit mp characteristics - <i>signal mode, CMRR, impedance, slew rate, e</i> basic Op-amp configurations <ul> <li>Inverting input amplifier</li> <li>Non-inverting input amplifier</li> <li>Voltage follower</li> </ul> </li> </ul>	(6 hrs) tc.

7.	<b>Op-An</b> 7.1. 7.2. 7.3. 7.4. 7.5.	Scaling adder Comparators	(6 hrs)
8.	Oscilla 8.1. 8.2. 8.3. 8.4. 8.5. 8.6. 8.7. 8.8.	Feedback Oscillators	(6 hrs)
9.	<b>Unreg</b> 9.1. 9.2. 9.3. 9.4. 9.5. 9.6. 9.7.	Emitter follower regulator Variable feedback regulator	(6 hrs)
	10.1. 10.2. 10.3. 10.4. 10.5. 10.6.	Shockley diodes ( or Four-layer diodes)	(6 hrs)
		In-Class Theory Hours *: ding quizzes, Midterm exam, and review time	56 hours)

## **LABORATORY EXERCISES**

There will be 13 lab exercises to be completed, one per week of the semester. Each exercise will be of 2 hours duration and all must be completed satisfactorily in order to gain a credit for ELEN 144. Labs will be available on D2L on Monday every week. Preparation must be completed by the student before the start of the lab.

### (Online Version)

Lab 01	Get Ready for Online Labs with MULTISIM
Lab 02	Introduction to Diodes and Diode Characteristics
Lab 03	Zener Diodes and Their Characteristics
Lab 04	Introduction to Bipolar Junction Transistors (BJTs)
Lab 05	A Simple DC-biased BJT Amplifier Design
Lab 06	Voltage-divider Biased BJT Circuits
Lab 07	BJT ac Amplifiers
Lab 08	Introduction to Field-Effect Transistors (FETs)
Lab 09	FET Amplifiers Design
Lab 10	Introduction to Operational Amplifiers (Op-amps)
Lab 11	Op-amps Applications
Lab 12	Oscillator Circuits
Lab 13	Introduction to Voltage Regulation Circuits
Lab 14	Introduction to Thyristors Circuits

## **EVALUATION**

Assignments/Quizzes	20%
Labs	20%
Midterm Exam	20%
Final Exam	40%
Total Course Mark	100%

**<u>GRADING</u>** (in accordance with the College policy)

A+	90 - 100%	В-	70 - 72%
Α	85 - 89%	<b>C</b> +	65 - 69%
А-	80-84%	С	60 - 64%
B+	77 - 79%	D	50 - 59%
B	73 - 76%	F	< 50%

A <u>minimum of 60%</u> overall mark must be achieved in both the theory and lab portions to pass the course. Less than 60% overall mark in either portion will result in a failure of the entire course. In addition, to pass the course, the final examination mark must be not less than 50%.

The final grading is based on 80% of theory work, and 20% of lab evaluation. Lab evaluation will be based on completing all assigned exercises and lab reports. Labs are to be completed within the assigned lab period and evaluated as satisfactory or unsatisfactory. Any unsatisfactory lab reports must be redone until a satisfactory level is achieved.

Attendance and completion of all lab material is mandatory to complete the course. Attendance at all tutorials is also compulsory.

Quizzes may be given at any time without prior notice and will be based on the current class notes, example problems and any textbook reading assigned.

### TEXTS AND REFERENCES

- Electronic Devices 6<sup>th</sup> Edition (or newer) Floyd Thomas L. ISBN 0-13-028484-X
- Laboratory Exercises, Handouts and Course Outline Typically available both online and in class

**Optional Book available:** (Used for ELEN 142 – Circuit Analysis)

• **Circuit Analysis with Devices** 2<sup>nd</sup> Edition (or newer) Robbins and Miller ISBN 140187984-5