

ELECTRONICS & COMPUTER ENGINEERING DEPARTMENT

(COURSE OUTLINE)

ELEN 142 CIRCUIT ANALYSIS

This course introduces students to concepts of circuit analysis for AC and DC circuits. Topics include: fundamental electrical quantities, series and parallel circuits, network analysis and theorems, resistance, capacitance and inductance. Instruments, instrumentation and troubleshooting concepts are introduced. Use is made of complex numbers and phasor diagrams to explain the operation of AC circuits.

OFFERED CREDIT: IN-CLASS WORKLOAD: OUT-OF-CLASS WORKLOAD: PREREQUISITES:	6	4 4 lecture, 1 tutorial, 2 lab		
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OBJECTIVE

Upon completion of this course the student will have a complete understanding of DC and AC circuit operation, including the analysis of circuits containing capacitors and inductors.

<u>OUTI</u>	LINE		Estimated Time
1.	Intro	duction (review)	(1 hour)
	1.1 1.2	SI Units Scientific Notation and Engineering Notation	
2	2 <u>Nature of Electricity (review)</u>		(1 hour)
	2.1 2.2	Theory of Electrical Charge Structure of the Atom 2.2.1 Bohr's Model and Structure of atoms and ions 2.2.2 States of Matter and Bonding	
	2.3 2.4 2.5	Conductors, Insulators and Semi-Conductors Conventional versus Electron Current Flow Definition of the Coulomb, Ampere, Volt and Ohm	

Types of Resistors including Linear Resistors and Non-Linear Resistors

4.3

3.

4.

Resistance

3.1

3.2 3.3

3.4

4.1

4.2

- 4.4 Parallel Circuits
- 4.5 Kirchhoff's Current Law (KCL)

Kirchhoff 's Voltage Law

Voltage Divider Rule

Resistive Networks and Simple Circuit Analysis

4.6 Current Divider Rule

Series Circuits

4.7 Series - Parallel Circuits

Resistor Color Code

Ohm's and Watt's Law

4.8 The Voltage Divider – two resistors, a potentiometer

Work, Energy, Power in Resistive Circuits

4.9 Voltmeter, Ammeter, Ohmmeter and other DC Measuring Instruments

(KVL)

- 4.10 Wheatstone Bridge Circuit
- 4.11 Delta Wye Conversions

5. <u>Circuit Analysis using Basic Network Theorems</u>

- 5.1 Equivalent Circuits
- 5.2 Constant Voltage Sources
- 5.3 Practical Voltage Sources
- 5.4 Internal Resistance
- 5.5 Constant Current Sources
- 5.6 Current Sources in Parallel and Series
- 5.7 Maximum Power Transfer Theorem
- 5.8 Thevenin's Theorem
- 5.9 Norton's Theorem* (optional)
- 5.10 Superposition Theorem
- 5.11 Mesh Current Analysis

6. <u>Capacitance</u>

- 6.1 Electric Fields
- 6.2 Electrostatic Induction
- 6.3 Dielectrics
- 6.4 Capacitance
- 6.5 Capacitors in Series
- 6.6 Capacitors in Parallel

(3 hours)

(7 hours)

(9 hours)

(2 hours)

7.	Capac	(7 hours)	
	7.1	Charging/Discharging	
	7.2	Time Constant	
	7.3	Stored Energy	
	7.4	Capacitor Response to step and rectangular inputs	
	7.5	Capacitor as an Integrator and Differentiator	
8.	Induct	tance	(2 hours)
	8.1	Electromagnetic Inductance	
	8.2	Faraday's Law	
	8.3	Lenz's Law	
	8.4	Self-Inductance	
	8.5	Inductors in Series	
	8.6	Inductors in Parallel	
9.	Induct	tance in DC CIRCUITS	(4 hours)
	9.1	Rise/Fall of Current in an RL Circuit	
	9.2	Time Constant	
	9.3	Stored Energy	
	9.4	RL Circuit Response to a Step input	
10.	Introd	uction to Alternating Current	(4 hours)
	10.1	Sine Wave Generation and Phase Relationships	
	10.1	Period, Frequency and Phasor Representations of Sine Waves	
	10.3	Purely Resistive AC Circuits	
	10.4	Peak, Average, and Effective (RMS) Value of A Sine Wave	
	10.5	Other Types of Periodic Waveforms	
11.	Capac	itance in AC Circuits	(5 hours)
	11.1	Capacitive Reactance	
	11.2	Analysis of Series RC Circuits	
	11.3	Analysis of Parallel RC Circuits	
	11.4	Power in a Capacitive Circuit	

12.	Induct	Inductance in AC Circuits	
	12.1	Inductive Reactance	
	12.2	Analysis of Series RL Circuits	
	12.3	Analysis of Parallel RL Circuits	
	12.4	Power in an Inductive Circuit	
13.	<u>Non F</u>	Resonant AC Circuits	(4 hours)
	13.1	Analysis of Series RLC Circuits	
	13.2	Analysis of Parallel RLC Circuits	
	13.3	Power in an RLC Circuit	
14.	Reson	ant AC Circuits	(6 hours)
	10001		(0 110 112)
	14.1	Series Resonance	
	14.2	Quality Factor & Selectivity in A Series Resonant Circuit	
	14.3	Parallel Resonance	
	14.4	Quality Factor & Selectivity in A Parallel Resonant Circuit	
15.	Transt	formers	(4 hours)
10.	<u>Transformers</u>		(Thous)
	15.1	Theory of Operation - Mutual Inductance	
	15.2	Iron, Air, and Ferrite Core Transformers	
	15.3	Voltage and Current Ratios	
	15.4	Reflected Impedance	
	15.5	Transformer Losses	
16.	Filters	5	(6 hours)
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	16.1	Low Pass Filters	
	16.2	High Pass Filters	
	16.3	Gain, Attenuation, Decibel, Decade, and Octave	
	16.4	Normalized Frequency Response Curves vs. Bode-plots	
	16.5	Band Pass Filters	
		Band Pass Filters Band Reject Filters (or Notch Filters)	
	16.5		
	16.5 16.6		70 hours

LABORATORY EXERCISES

There will be a total of 13 lab exercises to be completed, one per week of the semester. Each exercise will be of 2 hours duration and all must be completed satisfactorily in order to gain a credit for ELEN 142. All labs will be handed out during class in the week prior to the exercise and preparation must be completed by the student before the start of the lab.

- 1. Introduction to Multisim Software to ELEN-142
- 2. Breadboards, Resistors and Simple Circuit Construction
- 3. Simple Series Circuit and Voltage Divider
- 4. Parallel and Series-Parallel Circuits
- 5. DC Network Theorems
- 6. DC Capacitive Circuits
- 7. DC Inductive Circuits
- 8. Introduction to AC Measurements
- 9. AC Measurements in a Series RC Circuit
- 10. Amplitude, Phase Angle and Power in a Series AC Circuit
- 11. Resonant and Non-Resonant RLC Circuits
- 12. Transformer Characteristics
- 13. Filters

EVALUATION

Assignments, Quizzes and Tutorials	15%
Labs (13)	15%
Term Exams (2)	30%
<u>Final Exam</u>	40%
Total Course Mark	100%

<u>GRADING</u> (in accordance with the College policy):

A+	90 - 100%	В-	70 - 72%
Α	85 - 89%	C +	65 - 69%
A-	80 - 84%	С	60 - 64%
B +	77-79%	D	50 - 59%
B	73 - 76%	F	< 50%

A <u>minimum of 60%</u> overall mark must be achieved in both the theory and lab portions to pass the course. Less than 60% overall mark in either portion will result in a failure of the entire course. In addition, to pass the course, the final examination mark must be not less than 50%.

The final grading is based on 85% of theory work, and 15% of lab evaluation. Lab evaluation will be based on completing all assigned exercises and lab reports. Labs are to be completed within the assigned lab period and evaluated as satisfactory or unsatisfactory. Any unsatisfactory lab reports must be redone until a satisfactory level is achieved.

Attendance and completion of all lab material is mandatory to complete the course. Attendance at all tutorials is also compulsory.

Quizzes may be given at any time without prior notice and will be based on the current class notes, example problems and any textbook reading assigned.

COURSE TEXT	-	Introduction to Circuit Analysis		
		Walls Johnstone	ISBN 0-314-93386-7	
(or)	-	· ·	Devices 2 nd Edition (or newer) ISBN 140187984-5	

- Laboratory Exercises, Handouts and Course Outline